

FIG. 4A

FIG. 4B

FIG. 4

ELEMENT S (S) (S) (S) (S) (A) (G) (O)
ELEMENT R & C C C C
ELEMENT Q 6 8 8
ELEMENT P (2) (2) (2) (4) (4) (6) (9)
ELEMENT O SOSS
ELEMENT N (S)
ELEMENT M SSSS QB Q
ELEMENT L (3) (3) (2) (3) (4) (4) (4) (4) (4) (4) (4) (4) (4) (4
ELEMENT K QQQQQQQ
ELEMENT J S S S S Q B
ELEMENT / (2) (2) (2) (4) (4) (4) (4) (4) (4) (4) (4) (4) (4
ELEMENT H (Q Q Q ····· Q Q ····· Q
ELEMENT G & & &
ELEMENT F DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD
ELEMENT E DO
ELEMENT D 6 8 8
ELEMENT C 3 3 3 4 6
ELEMENT B @ @ @
ELEMENT A Q Q Q Q Q Q (Q)

EG. 50 IT LINE IMAGE

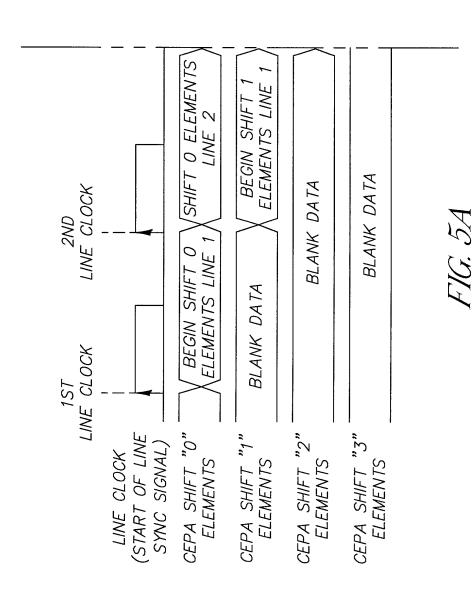
EG. 2-BIT CEPA (m=2) #STEPS=2^m #STEPS=4

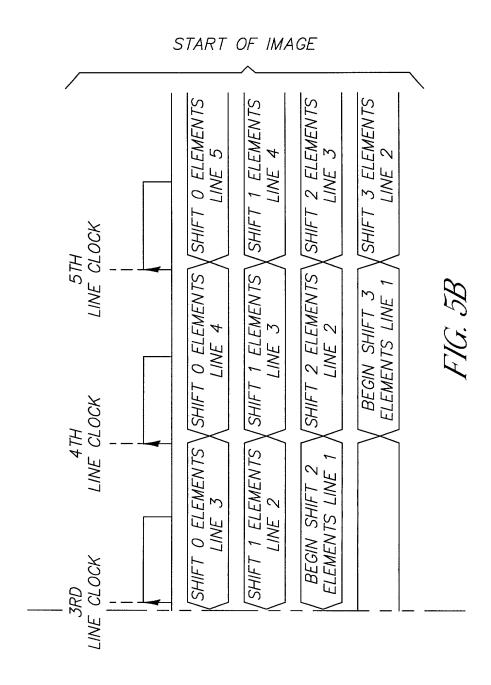
CEPA DATA LINE 1	CEPA DATA LINE 2 CEPA DATA LINE 3	CEPA DATA LINE 4 CEPA DATA LINE 5	PA DATA LINE 6		CEPA DATA LINE 49	CEPA DATA LINE 50	CEPA DATA LINE 51	CEPA DATA LINE 52	CEPA DATA LINE 53	
S) CO	(S)(S)	(S)(S)	SO CEPA		(4) (2)	(S)	(O)	(0)	(O)	_
0		23 (23 23 (83) 23 (84)	4 RS	• • • •	348	849	960	0	0	-
6		00	640		434	48/48	49(49)	49/60/60	0	_
0			(S) :		46/46	4	48 48	(4) (4) (4)	60 60	1
		E E	(A)		69	504	0	0	9	4B
	00	0 0 0	(2) E		4)48	48/49	(49) (50)	60 60	0	FIG.
0	(2) (3) (3) (3)	4 5 5 4 5	6)(5)		49(48)	50 49	0	0	0	
6			(E)		48/4	495	(S)	0		-
	0 (2)	(2) (2) (3) (3)	(F4) (C4)		(4) (4)	48 48	64 64	60 60	0	_
6	00		<i>A</i>		4647	3/4 //	9 48 49	969	0	_
			0	• • • •	404	4748	484	49(50	00	
		42 80	(3) (8)		46 46	1747	48 48	6464	50 50	_
						4				_

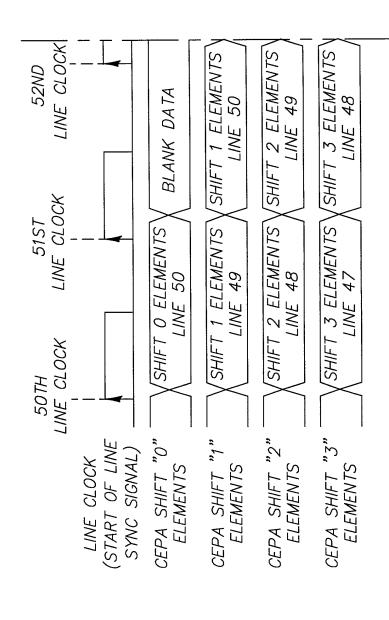
EG. CEPA OUTPUT IT LENGTH +(2^m -1) 50 + 3=53 53 IT LINE IMAGE

FIG. 5A	FIG. 5B	5B
FIG. 5C	FIG. 5D	50

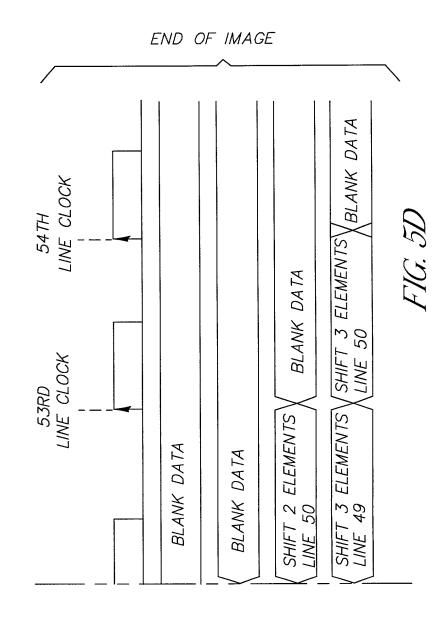
FIG. 5







+16.5C



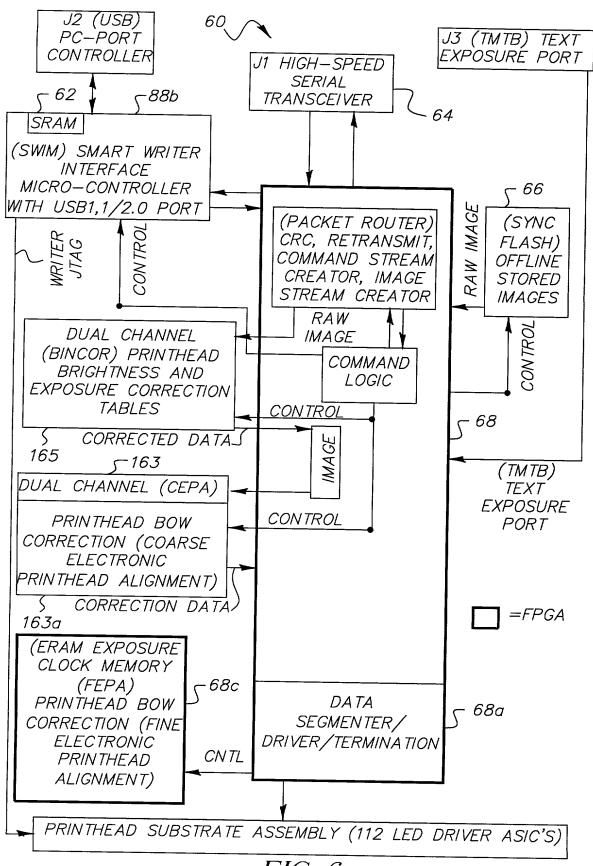
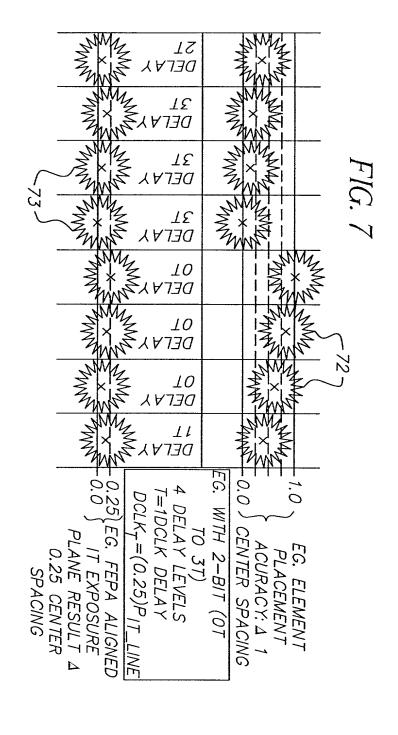


FIG. 6



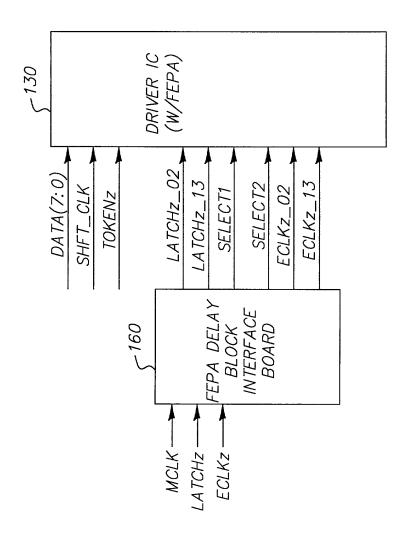
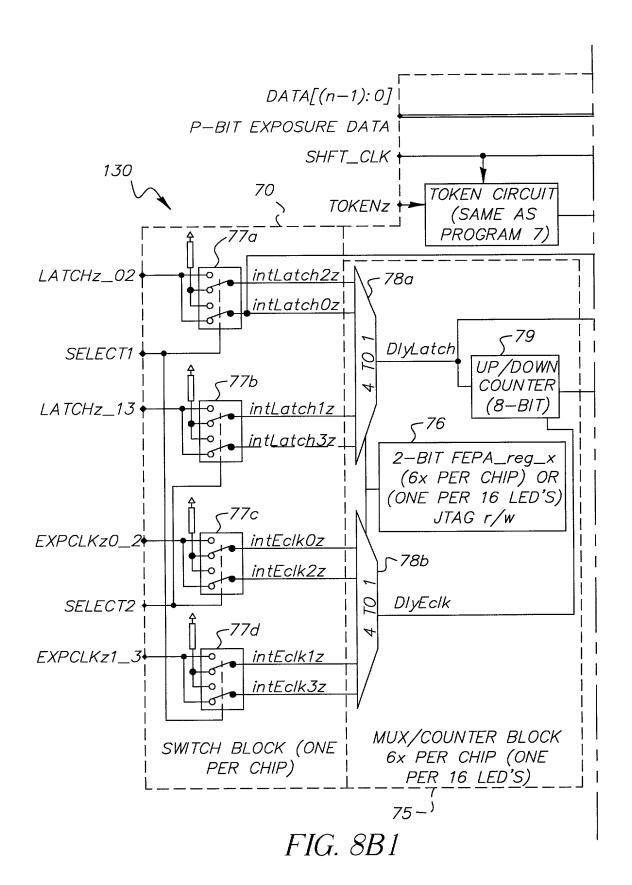


FIG. 8A

FIG. 8B1 FIG. 8B2

FIG. 8B



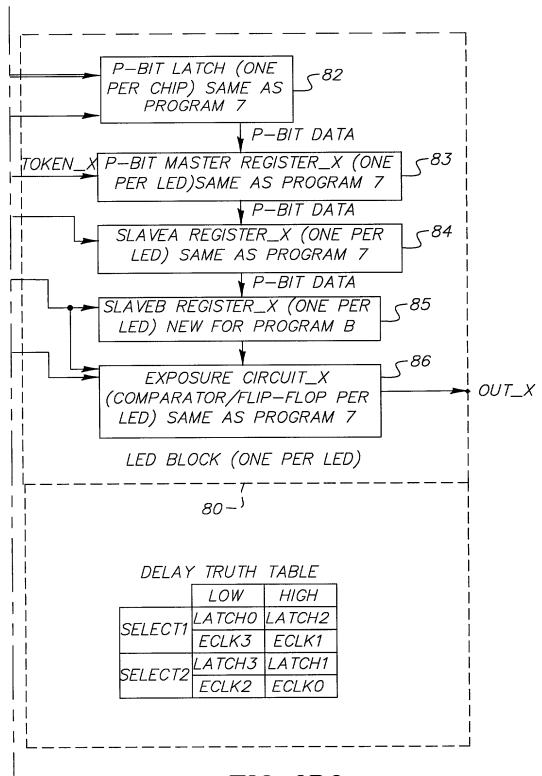


FIG. 8B2

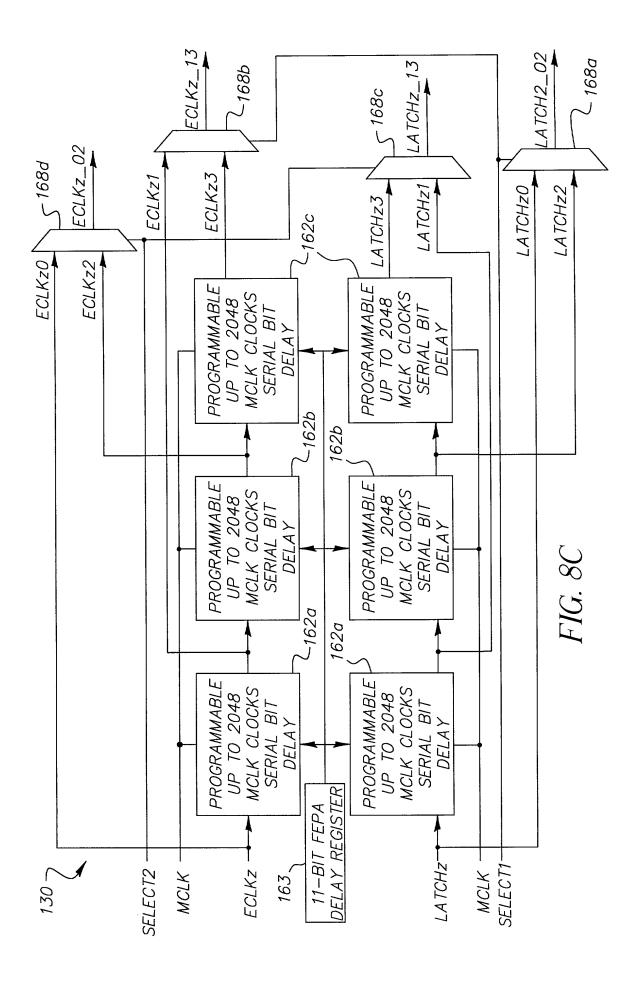
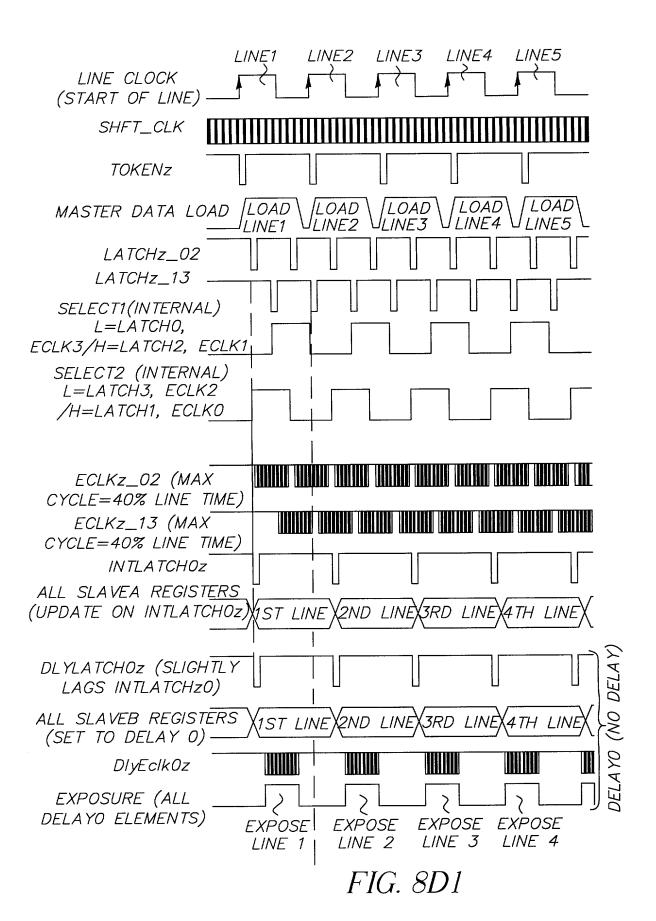


FIG. 8D1

FIG. 8D2

FIG. 8D



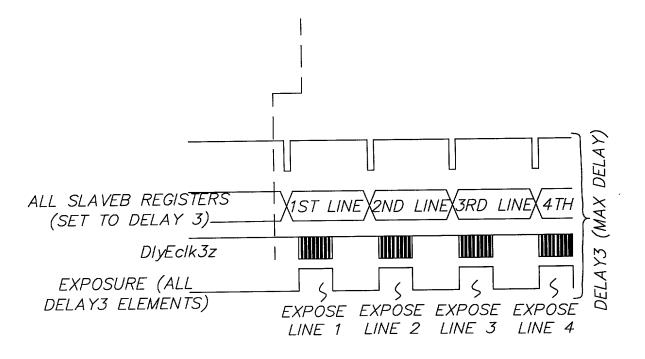


FIG. 8D2

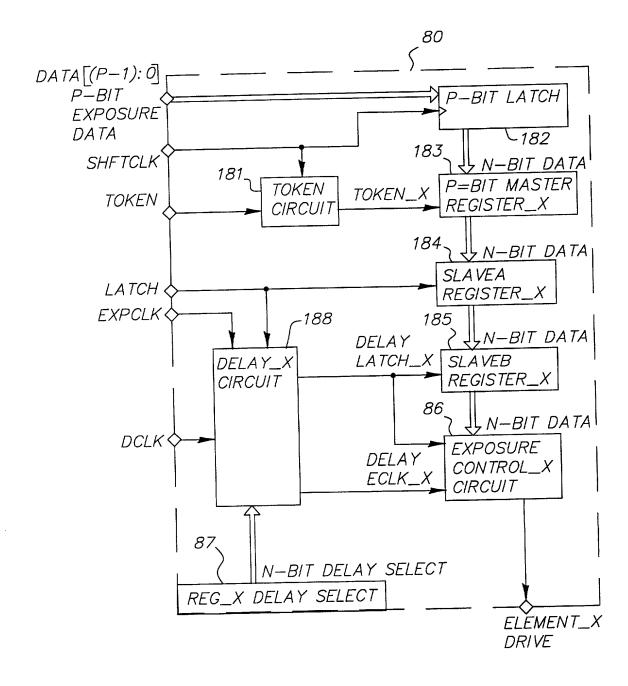


FIG. 9A

FIG. 9B1

FIG. 9B2

FIG. 9B

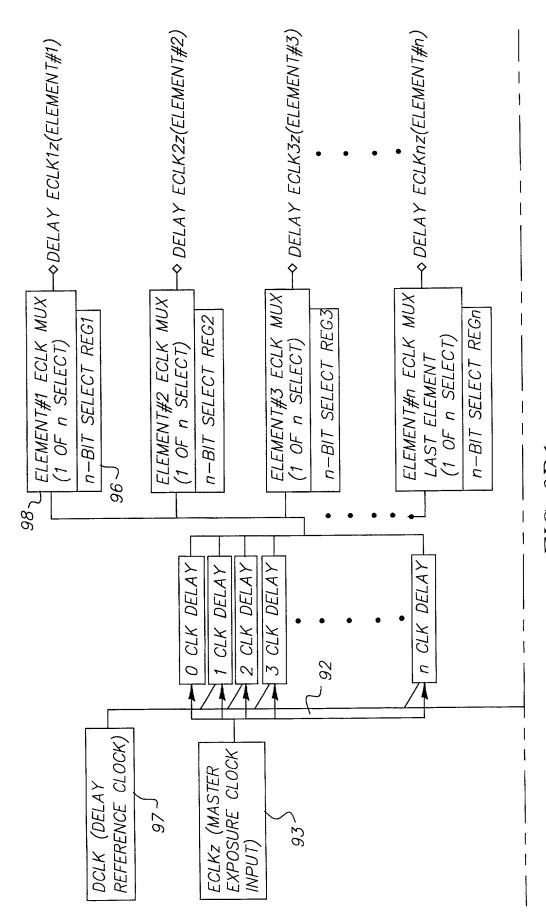


FIG. 9B1

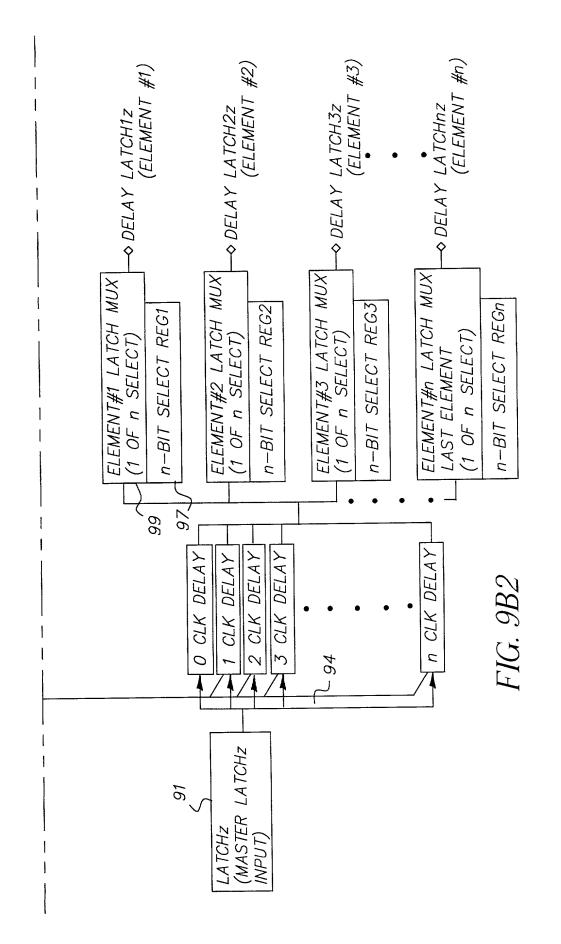


FIG. 10A

FIG. 10B

FIG. 10

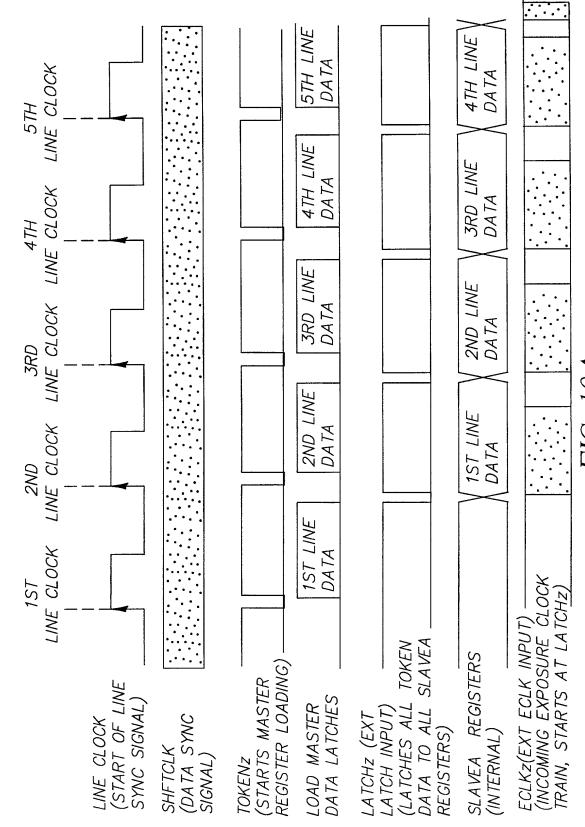


FIG. 10A

DELAY)(LAICHES SLAVEA DATA TO SLAVEB REGISTER) SLAVEB REGISTER (INTERNAL) (ELEMENTS W/ZERO DELAY) DELAY ECLKZ (ZERO DELAY) (INTERNAL DELAYED EXPOSURE CLOCK) ELEMENT EXPOSURE (ELEMENTS WITH ZERO DELAY)	1ST LINE DATA ::::::: EXPOSE LINE 1	ZND LINE DATA STAND LINE DATA STAND STAN	3RD LINE DATA EXPOSE LINE 3		DELAYED ELEMENT EXAMPLE OF ZERO
DELAY LATCH_x (MAX DELAY) (XFERS SLAVEA DATA TO SLAVEB REGISTER)					
SLAVEB REGISTER_X (INTERNAL) (ELEMENTS W/ MAX DELAY)		1ST LINE	ZND LINE	3RD LINE \\4TH	
DELAY ECLKZ_x (MAX DELAY) (INTERNAL DELAYED EXPOSURE CLOCK)					DETAY

FIG. 10B

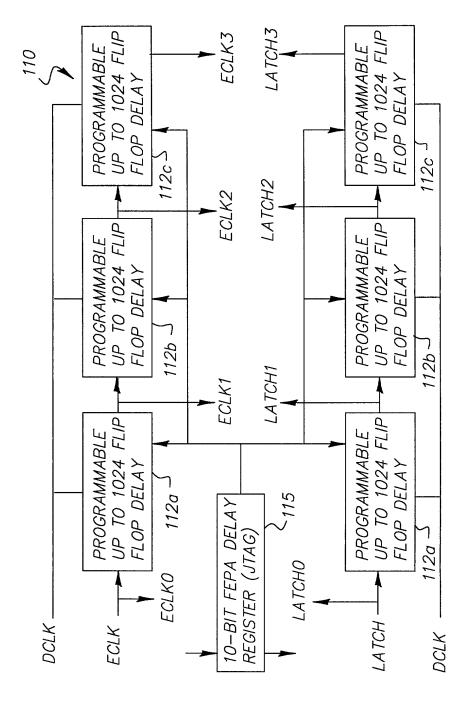


FIG. 11

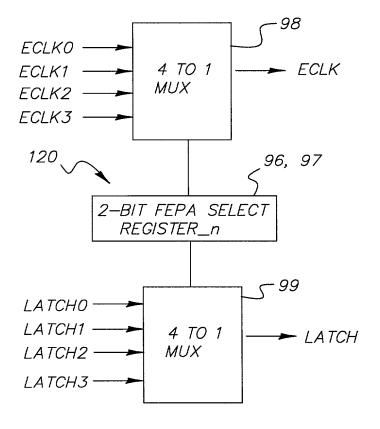
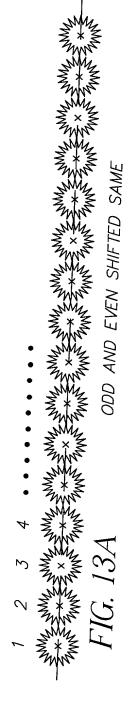
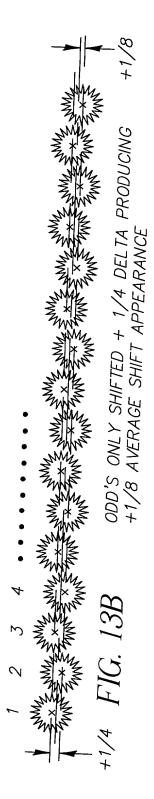


FIG. 12





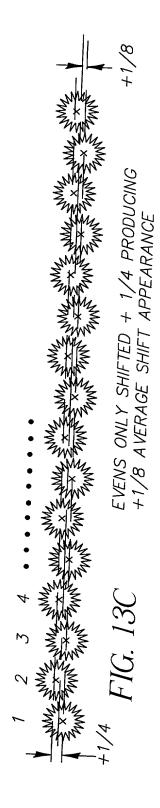
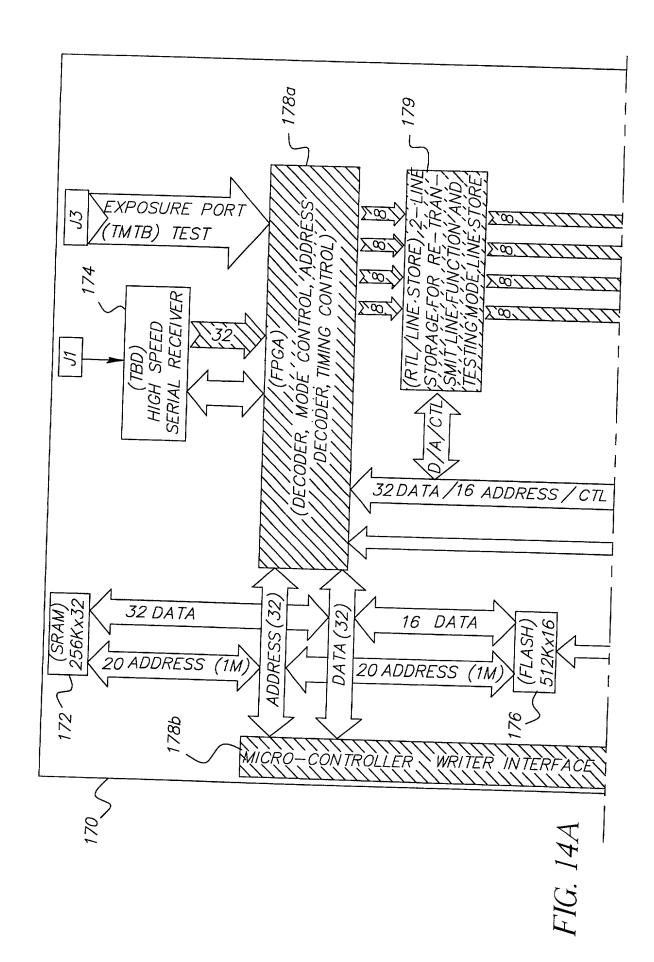


FIG. 14A

FIG. 14B

FIG. 14



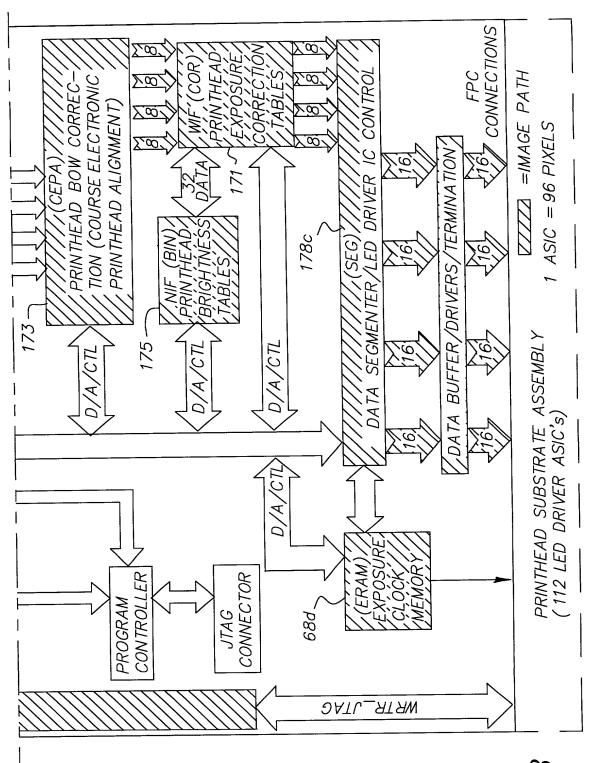


FIG. 14B